

**PATENT  
10/617,343****IN THE CLAIMS:**

Please cancel claims 13-20 and 24.

1. (Original) A surface-mountable device comprising:
  - (a) a first chip comprising lower and upper electrical contacts;
  - (b) a second chip comprising lower and upper electrical contacts;
  - (c) a first lead frame portion comprising a header region and a lead region, wherein the lower contact of the first chip is in electrical and mechanical connection with the header region of the first lead frame portion;
  - (d) a second lead frame portion comprising a header region and a lead region, wherein the lower contact of the second chip is in electrical and mechanical connection the header region of the second lead frame portion;
  - (e) a conductive member disposed between and in electrical and mechanical connection with the upper electrical contact of the first chip and the upper electrical contact of the second chip; and
  - (f) packaging material encapsulating at least a portion of (i) each of the first and second chips, (ii) the header regions of the first and second lead frame portions, and (iii) the conductive member,wherein the lead regions of the first and second lead frame portions extend from said packaging material and are adapted to allow the device to be surface-mounted with another electrical component, and wherein the first and second chips are not stacked upon each other within the packaged device.
2. (Original) The surface-mountable device of claim 1, wherein the header regions of the first and second lead frame portions do not overlap one another within the device.

**PATENT**  
**10/617,343**

3. (Original) The surface-mountable device of claim 1, wherein the header regions of the first and second lead frame portions are substantially coplanar within the device.
4. (Original) The surface-mountable device of claim 1, wherein the first and second lead frame portions each comprises detachment regions, which correspond to locations of separation from a precursor lead frame.
5. (Original) The surface-mountable device of claim 1, wherein the conductive member is a wire.
6. (Original) The surface-mountable device of claim 1, wherein the conductive member is a conductive sheet.
7. (Original) The surface-mountable device of claim 1, wherein a plurality of conductive members are disposed between and in electrical and mechanical connection with the upper electrical contact of the first chip and the upper electrical contact of the second chip.
8. (Original) The surface-mountable device of claim 1, wherein the first and second chips correspond to diodes.
9. (Original) The surface-mountable device of claim 8, wherein the diodes are rectifier diodes.
10. (Original) The surface-mountable device of claim 8, wherein the diodes are avalanche breakdown diodes.

**PATENT**  
**10/617,343**

11. (Original) The surface-mountable device of claim 1, wherein the first chip corresponds to an avalanche breakdown diode and wherein the second chip corresponds to a rectifier diode having a lower capacitance than the avalanche breakdown diode.
12. (Original) The surface-mountable device of claim 1, wherein the first chip corresponds to a thyristor surge suppressor and wherein the second chip corresponds to a rectifier diode having a lower capacitance than the thyristor surge suppressor.
- 13-20. (Canceled)
21. (Original) The surface-mountable device of claim 1, wherein the lower and upper electrical contacts are on opposing surfaces of the first and second chips.
22. (Original) The surface-mountable device of claim 1, wherein said device is a TVS device.
23. (Original) The surface-mountable device of claim 1, wherein said device is a low-capacitance, high-voltage TVS device.
24. (Canceled)
25. (Original) A method of manufacturing the surface-mountable device of claim 1, comprising:
- (a) providing a precursor lead frame comprising said first lead frame portion, said second lead frame portion, and a severable portion;
  - (b) placing said lower contact of said first chip in electrical and mechanical connection with the header region of the first lead frame portion and placing said lower contact of said second chip in electrical and mechanical connection with the header region of the second lead frame portion;

**PATENT**  
**10/617,343**

(c) placing said conductive member in electrical and mechanical connection with the upper electrical contact of the first chip and the upper electrical contact of the second chip;

(d) encapsulating at least a portion of (i) the first and second chips, (ii) the header regions of the first and second lead frame portions, and (iii) the conductive member in said packaging material; and

(e) separating the first and second lead frame portions from the severable portion of the precursor lead frame.